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Application Number:	10799783			
International Application Number:				
Confirmation Number:	8323			
Title of Invention:	Internal voltage generating circuit for semiconductor device			
First Named Inventor/Applicant Name:	Jin-Kyoung Jung			
Correspondence Address:	Steven M. Mills MILLS & ONELLO LLP Suite 605 Eleven Beacon Street Boston MA 02108 US 6179944900 -			
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File Listing:	

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)			
1 Miscellaneous Incoming Letter	Miscellaneous Incoming Letter	0529 trasmittal. pdf	63178	no	1			
	oszstiasilitaiipai	dd4d1e34229ec45f0e0e2defb4ee4e2df54c f0b3		· 				
Warnings:								
Information:								
2		0529 amend mentaf. pdf	1696934	yes	33			
			6d64f9620925483a1ba6c483eb04476718c 35e63					
	Multipart Description/PDF files in .zip description							
	Document Description		Start	End				
	Amendment After Final		1	1				
	Claims		2	15				
	Applicant Arguments/Remarks Made in an Amendment		16	33				
Warnings:								
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		Total Files Size (in bytes	17	60112				

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.